

TFT LCD Approval Specification

MODEL NO.: N141X6 - L06

Customer:	
Approved by :	
Note:	

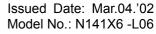
Liquid Crystal Display Division						
QRA Dept.	RD Dept.	PD Dept.				
Approval	Approval	Approval				
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REVISION HISTORY

Version	Date	Page (New)	Section	Description
2.0	Mar. 04. '02	All	All	Approval Specification was first issued.



Issued Date: Mar.04.'02 Model No.: N141X6 -L06

Approval

1. GENERAL DESCRIPTION

1.1 OVERVIEW

N141X6- L06 is a 14.1" TFT Liquid Crystal Display module with single CCFL Backlight unit and 20 pins LVDS interface. This module supports 1024 x 768 XGA mode and can display 262,144 colors. The optimum viewing angle is at 6 o'clock direction. The inverter module for Backlight is not built in.

1.2 FEATURES

- Thin and light weight and high brightness
- XGA (1024 x 768 pixels) resolution
- DE (Data Enable) only mode
- 3.3V LVDS (Low Voltage Differential Signaling) interface with 1 pixel/clock

1.3 APPLICATION

- TFT LCD Notebook

1.4 GENERAL SPECIFICATIONS

Item	Item Specification			
Active Area	285.7 (H) x 214.3 (V) (14.1" diagonal)	mm	(1)	
Bezel Opening Area	288.9 (H) x 217.5 (V)	mm	(1)	
Driver Element	a-si TFT active matrix	-	-	
Pixel Number	1024 x R.G.B. x 768	pixel	-	
Pixel Pitch	0.279 (H) x 0.279 (V)	mm	-	
Pixel Arrangement	RGB vertical stripe	-	-	
Display Colors	262,144	color	-	
Transmissive Mode	Normally white	-	-	
Surface Treatment	Hard coating (3H), Anti-glare (Haze 25)	-	-	

1.5 MECHANICAL SPECIFICATIONS

l1	tem	Min.	Тур.	Max.	Unit	Note
	Horizontal(H)	298.0	298.5	299.0	mm	
Module Size	Vertical(V)	226.0	226.5	227.0	mm	(1)
	Depth(D)	-	5.2	5.5	mm	
W	eight	-	470	485	g	-

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.



2. ABSOLUTE MAXIMUM RATINGS

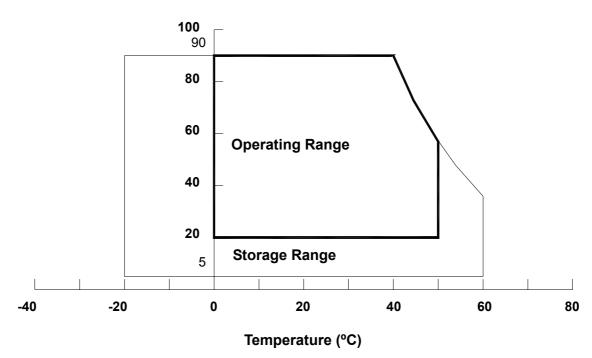
2.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Va	Unit	Note		
item	Syllibol	Min.	Max.	Offic	NOLE	
Storage Temperature	T _{ST}	-20	+60	°C	(1)	
Operating Ambient Temperature	T _{OP}	0	+50	°C	(1), (2)	
Shock (Non-Operating)	S _{NOP}	-	50	G	(3), (5)	
Vibration (Non-Operating)	V_{NOP}	-	1.5	G	(4), (5)	

Note (1) Temperature and relative humidity range is shown in the figure below.

- (a) 90 %RH Max. (Ta \leq 40 °C).
- (b) Wet-bulb temperature should be 39 °C Max. (Ta > 40 °C).
- (c) No condensation.
- Note (2) The temperature of panel surface should be 0 °C Min. and 60 °C Max.
- Note (3) 11ms, half sine wave, 1 time for $\pm X$, $\pm Y$, $\pm Z$.
- Note (4) $10 \sim 500 \sim 10$ Hz, 0.5 Hr/cycle, 1hr each X, Y, Z,
- Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.

Relative Humidity (%RH)





Issued Date: Mar.04.'02 Model No.: N141X6 -L06

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2.2 ELECTRICAL ABSOLUTE RATINGS

2.2.1 TFT LCD MODULE

Item	Symbol	vmbol Value		Unit	Note	
пеш	Syllibol	Min.	Max.	Offic	Note	
Power Supply Voltage	Vcc	-0.3	+4.0	V	(1)	
Logic Input Voltage	V_{IN}	-0.3	Vcc+0.3	V	(1)	

2.2.2 BACKLIGHT UNIT

Item	Symbol	Value		Unit	Note
item	Syllibol	Min.	Max.	Offic	Note
Lamp Voltage	V_L	-	2.5K	V_{RMS}	(1) , (2) , $I_L = 6.0 \text{ mA}$
Lamp Current	ΙL	2.0	7.0	mA_{RMS}	(1) (2)
Lamp Frequency	F_L	45	80	KHz	(1), (2)

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for lamp (Refer to 3.2 for further information).





3. ELECTRICAL CHARACTERISTICS

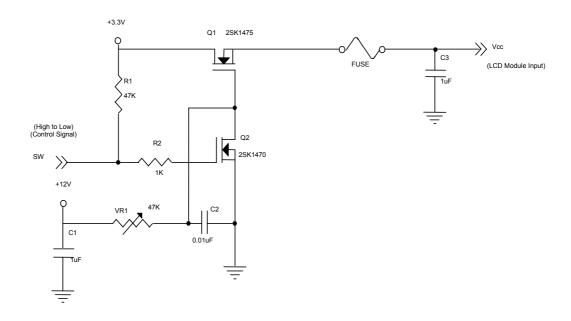
3.1 TFT LCD MODULE

Ta = 25 ± 2 °C

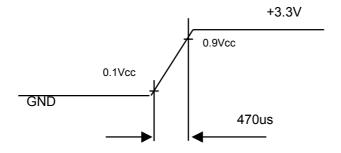
Parameter		Symbol		Value	Unit	Note	
		Symbol	Min.	Тур.	Max.	Offic	NOLE
Power Supply Voltage	Power Supply Voltage		3.0	3.3	3.6	V	-
Ripple Voltage		V_{RP}	-	50	-	mV	-
Rush Current		I _{RUSH}	-	1.0	1.5	Α	(2)
Power Supply Current		Icc	-	400	500	mA	
Logical Input Voltage	"H" Level	V _{IH}	-	-	+100	mV	-
Logical Input Voltage	"L" Level	V_{IL}	-100	_	-	mV	-
Terminating Resistor		R_T	-	100	-	Ohm	-

Note (1) The module should be always operated within above ranges.

Note (2) Measurement Conditions:



Vcc rising time is 470us



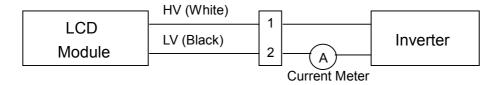


3.2 BACKLIGHT UNIT

Т٦	_	25	_	2	00
ıa	=	20	+	_	٣(.

Parameter	Symbol		Value	Unit	Note	
raiametei	Syllibol	Min.	Тур.	Max.	Offic	Note
Lamp Input Voltage	V_L	580	650	720	V_{RMS}	$I_{L} = 6.0 \text{ mA}$
Lamp Current	ΙL	2.0	6.0	7.0	mA_{RMS}	(1)
Lamp Turn On Voltage	Vs	ı	-	890 (25 °C)	V_{RMS}	(2)
Lamp rum on voltage		-	-	1130 (0 °C)	V_{RMS}	(2)
Operating Frequency	F_L	45	60	80	KHz	(3)
Lamp Life Time	L_BL	10,000	15,000	-	Hrs	(5)
Power Consumption	P_L	-	3.9	-	W	(4) , $I_L = 6.0 \text{ mA}$

Note (1) Lamp current is measured by utilizing a high frequency current meter as shown below:

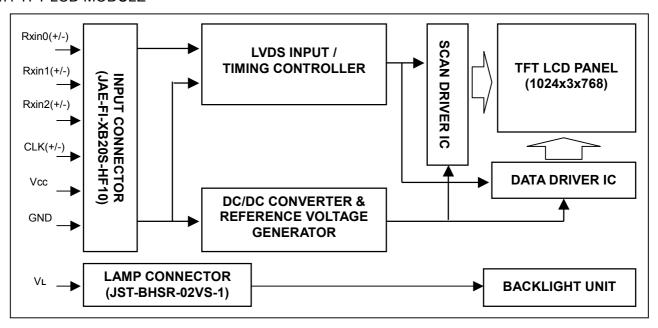


- Note (2) The voltage shown above should be applied to the lamp for more than 1 second after startup. Otherwise the lamp may not be turned on.
- Note (3) The lamp frequency may produce interference with horizontal synchronous frequency from the display, and this may cause line flow on the display. In order to avoid interference, the lamp frequency should be detached from the horizontal synchronous frequency and its harmonics as far as possible.
- Note (4) $P_L = I_L \times V_L$
- Note (5) The lifetime of lamp can be defined as the time in which it continues to operate under the condition Ta = 25 \pm 2 °C and I_L = 2.0 ~ 6.5 mArms until one of the following events occurs:
 - (a) When the brightness becomes or lower than 50% of its original value.
 - (b) When the effective ignition length becomes or lower than 80% of its original value. (Effective ignition length is defined as an area that has less than 70% brightness compared to the brightness in the center point.)
- Note (6) The waveform of the voltage output of inverter must be area-symmetric and the design of the inverter must have specifications for the modularized lamp. The performance of the Backlight, such as lifetime or brightness, is greatly influenced by the characteristics of the DC-AC inverter for the lamp. All the parameters of an inverter should be carefully designed to avoid producing too much current leakage from high voltage output of the inverter. When designing or ordering the inverter please make sure that a poor lighting caused by the mismatch of the Backlight and the inverter (miss-lighting, flicker, etc.) never occurs. If the above situation is confirmed, the module should be operated in the same manners when it is installed in your instrument.

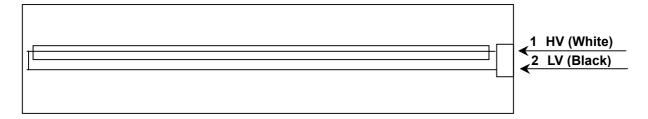


4. BLOCK DIAGRAM

4.1 TFT LCD MODULE



4.2 BACKLIGHT UNIT





5. INPUT TERMINAL PIN ASSIGNMENT

5.1 TFT LCD MODULE

Pin	Symbol	Description	Polarity	Remark
1	Vcc	Power Supply +3.3 V		-
2	Vcc	Power Supply +3.3 V		-
3	Vss	Ground		-
4	Vss	Ground		-
5	Rxin0-	LVDS Differential Data Input	Negative	R0~R5,G0
6	Rxin0+	LVDS Differential Data Input	Positive	10-13,60
7	Vss	Ground		-
8	Rxin1-	LVDS Differential Data Input	Negative	G1~G5,B0,B1
9	Rxin1+	LVDS Differential Data Input	Positive	G1-G5,B0,B1
10	Vss	Ground		-
11	Rxin2-	LVDS Differential Data Input	Negative	B2~B5,DE,Hsync,Vsync
12	Rxin2+	LVDS Differential Data Input	Positive	BZ**B3,DE,i isylic, vsylic
13	Vss	Ground		-
14	CLK-	LVDS Clock Data Input	Negative	LVDS Level
15	CLK+	LVDS Clock Data Input	Positive	LVD3 Level
16	Vss	Ground		-
17	NC	Non-Connection		-
18	NC	Non-Connection		-
19	Vss	Ground		-
20	Vss	Ground		-

Note (1) Connector Part No.: JAE-FI-XB20S-HF10 or equivalent

Note (2) User's connector Part No: JAE-FI-X20M or equivalent

Note (3) The first pixel is even.

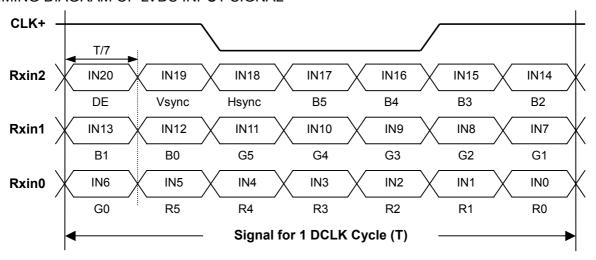
5.2 BACKLIGHT UNIT

Pin	Symbol	Description	Color
1	HV	High Voltage	White
2	LV	Ground	Black

Note (1) Connector Part No.: JST-BHSR-02VS-1 or equivalent

Note (2) User's connector Part No.: JST-SM02B-BHSS-1-TB or equivalent

5.3 TIMING DIAGRAM OF LVDS INPUT SIGNAL





5.4 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

	Color	Data Signal																	
			Re	ed					Gre	een					Bl	ue			
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Red	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
Gray	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Green	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Blue	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage



6. INTERFACE TIMING

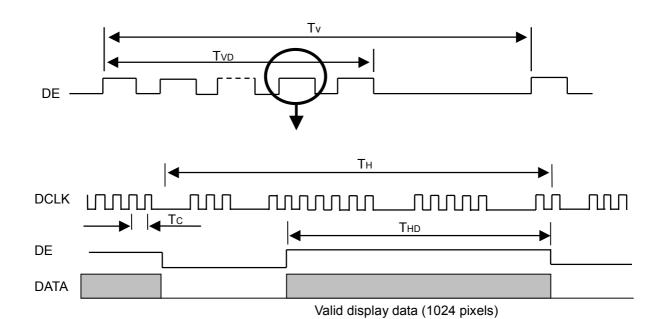
6.1 INPUT SIGNAL TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Clock	Frequency	1/Tc	-	65	-	MHz	-
Frame Frequency	Cycle	T _V	769	806	1000	lines	-
Vertical Active Display Term	Display Period	T_VD	768	768	768	lines	-
One Line Scanning Time	Cycle	T _H	1200	1344	1612	clocks	(1)
Horizontal Active Display Term	Display Period	T_{HD}	1024	1024	1024	clocks	-

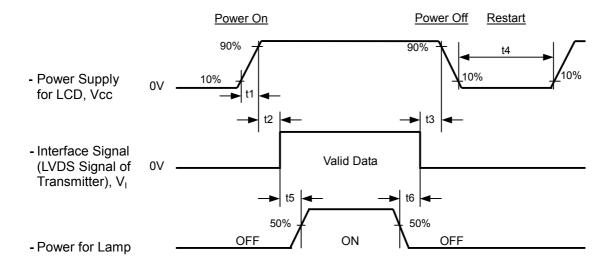
Note (1) The duration of DE signal must be longer than 1 clock period at every horizontal sync. period.

INPUT SIGNAL TIMING DIAGRAM



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6.2 POWER ON/OFF SEQUENCE



Timing Specifications:

 $0 < t1 \leq 15 \text{ msec}$

 $0\,\,<\,t2\,\leqq\,50\;msec$

 $0 < t3 \le 50 \text{ msec}$

t4 ≥ 500 sec

 $t5 \ge 200 \text{ msec}$

 $t6 \ge 0 \text{ msec}$

Note (1) Please avoid floating state of interface signal at invalid period.

Note (2) When the interface signal is invalid, be sure to pull down the power supply of LCD Vcc to 0 V.

Note (3) The Backlight inverter power must be turned on after the power supply for the logic and the interface signal is valid. The Backlight inverter power must be turned off before the power supply for the logic and the interface signal is invalid.



7. OPTICAL CHARACTERISTICS

7.1 TEST CONDITIONS

Item	Symbol	Value	Unit			
Ambient Temperature	Та	25±2	°C			
Ambient Humidity	На	50±10	%RH			
Supply Voltage	V_{CC}	3.3	V			
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTIC					
Inverter Current	Ι _L	6.0	mA			
Inverter Driving Frequency	F _L 60 KHz					
Inverter	Sumida-H05-4783B					

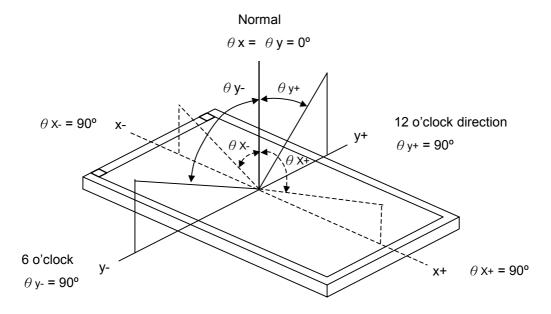
The relative measurement methods of optical characteristics are shown in 7.2. The following items should be measured under the test conditions described in 7.1 and stable environment shown in Note (6).

7.2 OPTICAL SPECIFICATIONS

Iten	า	Symbol	Condition	Min.	Тур.	Max.	Unit	Note	
Contrast Ratio		CR		150	250	-	-	(2), (6)	
Response Time		T_R		-	15	30	ms	(3)	
Response fille		T_F		-	35	50	ms		
Center Luminan	ce of White	L		150	180	-	cd/m ²	(6)	
Average Lumina	nce of White	L _{AVE}		140	170	-	cd/m ²	(4), (6)	
White Variation		δW		-	1.4	1.6	-	(6), (7)	
Cross Talk		CT	$\theta_{x} = 0^{\circ}, \theta_{Y} = 0^{\circ}$	-	-	4.0	%	(5), (6)	
	Red	Rx	Viewing Normal Angle	0.542	0.572	0.602	-		
		Ry	viewing Normal Angle	0.315	0.345	0.375	-		
	Green	Gx		0.285	0.315	0.345	-		
Color		Gy		0.501	0.531	0.561	-	(1), (6)	
Chromaticity	Blue	Bx		0.120	0.150	0.180	-		
		Ву		0.095	0.125	0.155	-		
	\//b:to	Wx		0.280	0.310	0.340	-		
	White	Wy		0.300	0.330	0.360	-		
	llowi-omtol	θ_x +		50	60	-			
Viowing Anglo	Horizontal	θ _x -	CR≧10	50	60	-	Dog		
Viewing Angle	Vertical	θ _Y +	UN≦ IU	25	35	-	Deg.		
	Vertical	θ _Y -		50	60	-			



Note (1) Definition of Viewing Angle (θx , θy):



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L63 / L0

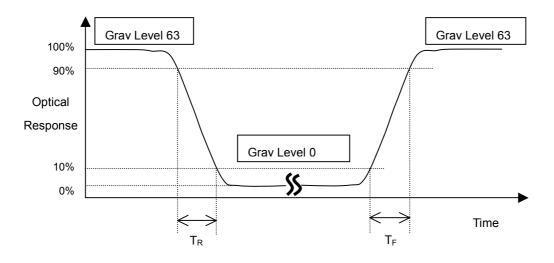
L63: Luminance of gray level 63

L 0: Luminance of gray level 0

CR = CR(5)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (7).

Note (3) Definition of Response Time (T_R, T_F) :





Note (4) Definition of Average Luminance of White (L_{AVE}):

Measure the luminance of gray level 63 at 5 points

$$L_{AVE} = [L(1) + L(2) + L(3) + L(4) + L(5)] / 5$$

L (x) is corresponding to the luminance of the point X at Figure in Note (7).

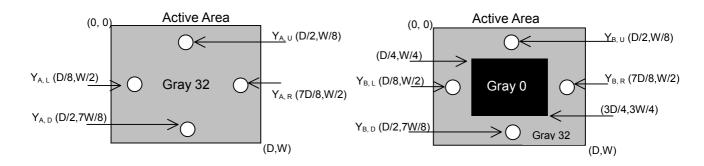
Note (5) Definition of Cross Talk (CT):

$$CT = | Y_B - Y_A | / Y_A \times 100 (\%)$$

Where:

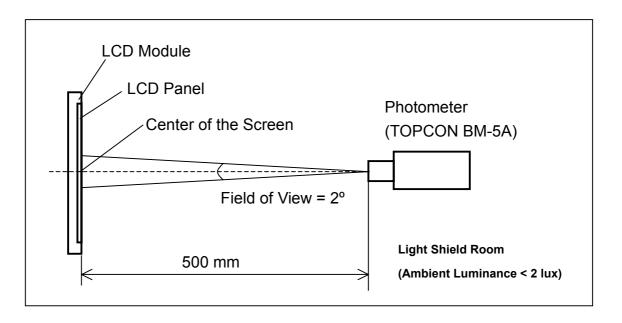
Y_A = Luminance of measured location without gray level 0 pattern (cd/m²)

Y_B = Luminance of measured location with gray level 0 pattern (cd/m²)



Note (6) Measurement Setup:

The LCD module should be stabilized at given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.

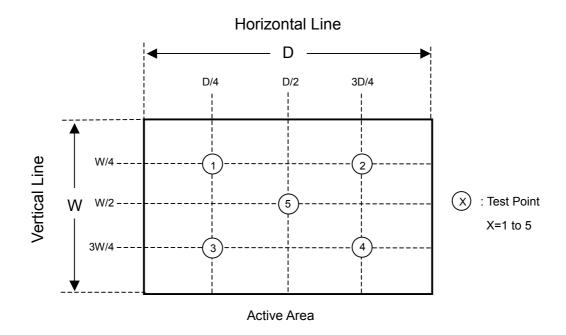




Note (7) Definition of White Variation (δ W):

Measure the luminance of gray level 63 at 5 points

 δ W = Maximum [L (1), L (2), L (3), L (4), L (5)] / Minimum [L (1), L (2), L (3), L (4), L (5)]





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8. PRECAUTIONS

8.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the module during assembly.
- (2) To assemble or install module into user's system can be only in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) It's not permitted to have pressure or impulse on the module because the LCD panel and Backlight will be damaged.
- (4) Always follow the correct power sequence when LCD module is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.
- (5) Do not pull the I/F connector in or out while the module is operating.
- (6) Do not disassemble the module.
- (7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (8) It is dangerous that moisture come into or contacted the LCD module, because moisture may damage LCD module when it is operating.
- (9) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
- (10) When ambient temperature is lower than 10°C may reduce the display quality. For example, the response time will become slowly, and the starting voltage of CCFL will be higher than room temperature.

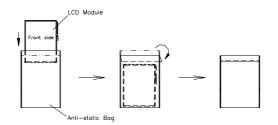
8.2 SAFETY PRECAUTIONS

- (1) The startup voltage of Backlight is approximately 1000 Volts. It may cause electrical shock while assembling with inverter. Do not disassemble the module or insert anything into the Backlight unit.
- (2) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (3) After the module's end of life, it is not harmful in case of normal operation and storage.

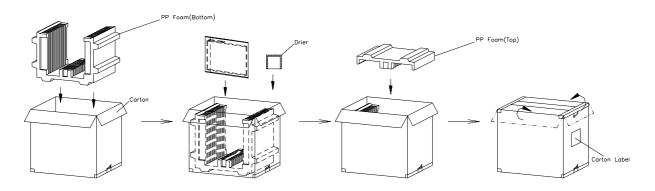


9. PACKING

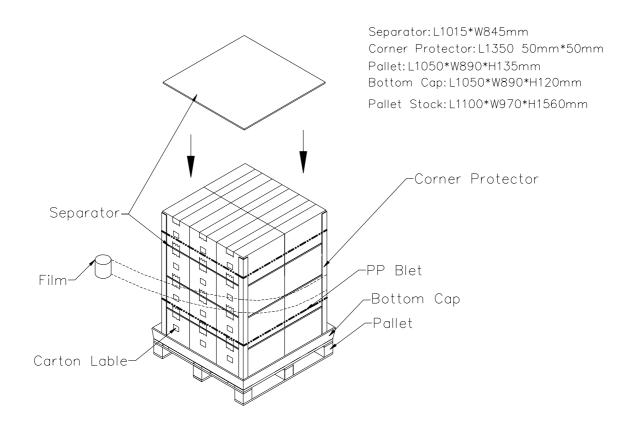
9.1 CARTON



Box dimensions: 422(L)x337(W)x345(H)mm Weight: Approx. 7.0Kg(10modules per 1 box)



9.2 PALLET





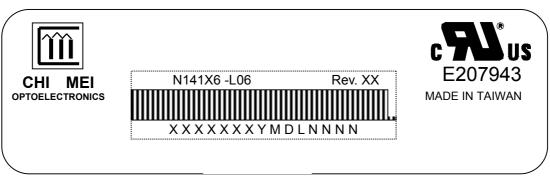
Issued Date: Mar.04.'02 Model No.: N141X6 -L06

Approval

10. DEFINITION OF LABELS

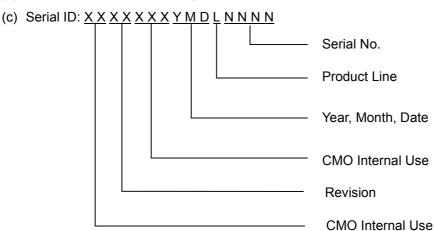
10.1 CMO MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



(a) Model Name: N141X6 - L06

(b) Revision: Rev. XX, for example: C1, C2 ...etc.



Serial ID includes the information as below:

(a) Manufactured Date: Year: 1~9, for 2001~2009

Month: 1~9, A~C, for Jan. ~ Dec.

Day: 1~9, A~Y, for 1st to 31st, exclude I, O and U

(b) Revision Code: cover all the change

(c) Serial No.: Manufacturing sequence of product

(d) Product Line: 1 -> Line1, 2 -> Line 2, ...etc.



CHIMEI OPTOELECTRONICS CORP.

Issued Date: Mar.04.'02 Model No.: N141X6 -L06 **Approval**

10.2 CARTON LABEL

